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**APPLICANTS**

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This application is a DIV of 09/877,530 06/08/2001 PAT 6,725,334  
 which claims benefit of 60/210,655 06/09/2000  
 and claims benefit of 60/210,675 06/10/2000

**\*\* FOREIGN APPLICATIONS \*\*\*\*\*****IF REQUIRED, FOREIGN FILING LICENSE GRANTED****\*\* 09/10/2004**

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature _____ Initials _____	STATE OR COUNTRY CA	SHEETS DRAWING 7	TOTAL CLAIMS 8	INDEPENDENT CLAIMS 2
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**ADDRESS**

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**TITLE**

Method and system for exclusive two-level caching in a chip-multiprocessor

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